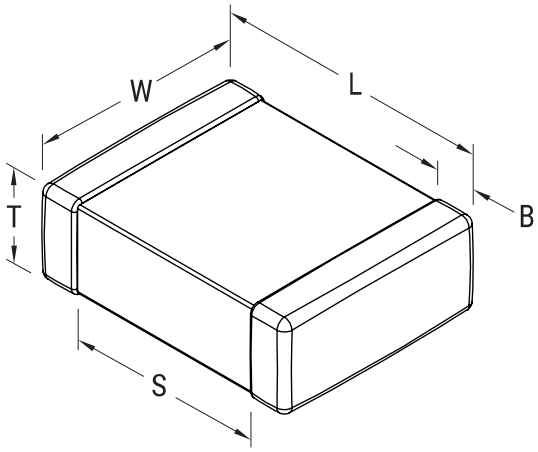
C0805S560JCGACTU

Aliases (C0805S560JCGAC7800)

SMD Comm C0G HV FE, Ceramic, 56 pF, 5%, 500 VDC, C0G, SMD, MLCC, FE-CAP, Floating Electrode, High Voltage, 0805 / 2012



Click here for the 3D model.

General Information	
Series	SMD Comm C0G HV FE
Style	SMD Chip
Description	SMD, MLCC, FE-CAP, Floating Electrode, High Voltage
Features	FE-CAP, Floating Electrode, High Voltage
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	14 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	1.25mm +/-0.15mm
S	0.7mm MIN
B	0.5mm +/-0.25mm
Case Code (EIA / mm)	0805 / 2012

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2500

Specifications	
Capacitance	56 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	500 VDC
Dielectric Withstanding Voltage	750 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	C0G
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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