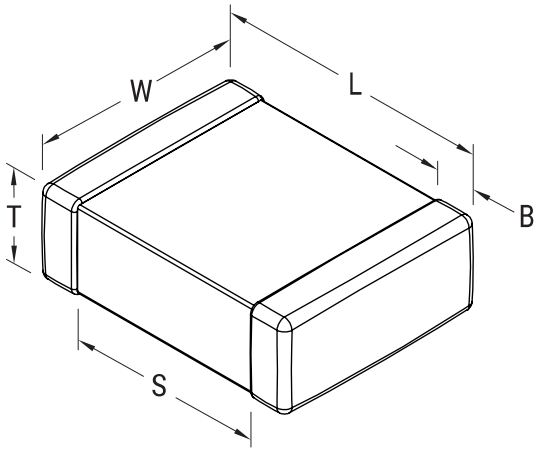
C0805T751F5GCC7138

SMD COTS C0G, Ceramic, 750 pF, 1%, 50 VDC, C0G, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805 / 2012



Click here for the 3D model.

General Information	
Series	SMD COTS C0G
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
Typical Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.78mm +/-0.10mm
S	0.7mm MIN
B	0.5mm +/-0.25mm
Case Code (EIA / mm)	0805 / 2012

Packaging Specifications	
Packaging	T&R, 330mm, Paper Tape
Packaging Quantity	15000

Specifications	
Capacitance	750 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	1%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	C0G
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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