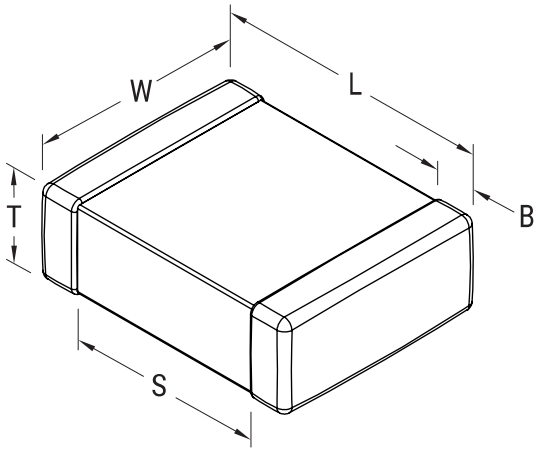
C1210S560JDGACTU

Aliases (C1210S560JDGAC7800)

SMD Comm C0G HV FE, Ceramic, 56 pF, 5%, 1,000 VDC, C0G, SMD, MLCC, FE-CAP, Floating Electrode, High Voltage, 1210 / 3225



Click here for the 3D model.

General Information	
Series	SMD Comm C0G HV FE
Style	SMD Chip
Description	SMD, MLCC, FE-CAP, Floating Electrode, High Voltage
Features	FE-CAP, Floating Electrode, High Voltage
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	70 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
L	3.2mm +/-0.2mm
W	2.5mm +/-0.2mm
T	1.7mm +/-0.20mm
S	1.5mm MIN
B	0.5mm +/-0.25mm
Case Code (EIA / mm)	1210 / 3225

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2000

Specifications	
Capacitance	56 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	1000 VDC
Dielectric Withstanding Voltage	1,200 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	C0G
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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