Innovative Service Around the Globe YAGEO

DATA SHEET

POSITIVE TEMPERATURE COEFFICIENT AC/DC POWER SUPPLY

PPTC Low Rho SMDL0402 Series

RoHS compliant & Halogen free



Product specification– September 25, 2025 V.4

Positive Temperature Coefficient (PTC) Data Sheet

Description

The Low Rho SMDL0402 Series Surface Mount PPTC (Polymer Positive Temperature Coefficient) devices provide overcurrent protection for applications where ultra low internal resistance, ultra low voltage drop and automatic resettable protection are desired. This new series allows a higher hold current device in a smaller factor and lower profile as compared to a standard PPTC. It offers ultra low internal resistance while maintaining the high level electrical characteristics and performances of standard PPTC products. And have maximum fault current of 40A. The SMDL0402 series provides miniature surface mount resettable overcurrent protection with holding current from 0.1A to 0.35A

Features

- Ultra low internal resistance
- Miniature size saves board space
- Fast response to fault currents
- Compatible with high temperature solders
- UL/CUL/ TUV/ RoHS/Halogen Free
- RoHS compliant, Lead Free and Halogen Free

Applications

- USB peripherals including new USB 3.0 / 2.0 ports
- Smart phones
- E-readers
- Computer peripherals
- Hard disk drives
- Li-ion / Li-Polymer battery packs
- Tablet and Notebook PCs
- LCD / LED HDTV
- Digital cameras and video cameras
- Game consoles

Electrical Characteristics

Part Number	I_{hold} (A)	I_{trip} (A)	V_{max} (Vdc)	I_{max} (A)	$P_{d\ typ.}$ (W)	Maximum Time To Trip		Resistance	
						Time (Sec.)	Current (A)	R_{min} (Ω)	R_{1max} (Ω)
SMDL0402B010TF	0.10	0.30	6	40	0.5	1.0	0.50	0.15	2.00
SMDL0402B020TF	0.20	0.50	6	40	0.5	1.0	1.00	0.10	1.25
SMDL0402B035TF	0.35	0.70	6	40	0.5	0.1	8.00	0.05	0.70

Note on Electrical Characteristics

- Vocabulary
  - I_{hold} = Hold current: maximum current device will pass without tripping in 23℃ still air.
  - I_{trip} = Trip current: minimum current at which the device will trip in 23℃ still air.
  - V_{max} = Maximum voltage device can withstand without damage at rated current (I_{max})
  - I_{max} = Maximum fault current device can withstand without damage at rated voltage (V_{max})
  - $P_{d\ typ.}$ = Typical power dissipated from device when in the tripped state at 23℃ still air.
  - R_{min} = Minimum resistance of device in initial (un-soldered) state.
  - R_{1max} = Maximum resistance of device at 23℃ measured one hour after tripping or reflow soldering of 260℃ for 20 sec.
- Value specified is determined by using the PWB with 0.030”*1.5oz copper traces.
- Caution: Operation beyond the specified rating may result in damage and possible arcing and flame.

Polymeric PTC Selecting Guide

■ Determine the following operating parameters for the circuits:

- Normal operating current (I_{hold})
- Maximum interrupt current (I_{max})
- Maximum circuit voltage (V_{max})
- Normal operating temperature surrounding device (min℃/max℃)

■ Select the device from factor and dimension suitable for the application

■ Compare the maximum rating for V_{max} and I_{max} of the PPTC device with the circuit in application and make sure the circuit's requirement does not exceed the device rating.

■ Check that PPTC device's trip time (time-to-trip) will protect the circuit.

■ Verify that the circuit operating temperature is within the PPTC device's normal operating temperature range.

■ Verify that performance and suitability of the chosen PPTC device in the application.

⚠ WARNING

■ Mechanical Stress

- PPTC devices will undergo a thermal expansion during fault condition. If PPTC devices are installed or placed in an application where the space between PPTC devices and the surrounding materials (e.g., covering materials, packaging materials, encapsulate materials and the like) is insufficient, it will cause an inhibiting effect upon the thermal expansion. Pressing, twisting, bending and other kinds of mechanical stress will also adversely affect the performance of the PPTC devices, and shall not be used or applied.

■ Chemical Pollutants

- Silicone-based oils, oils, solvents, gels, electrolytes, fuels, acids, and the like will adversely affect the properties of PPTC devices, and shall not be used or applied.

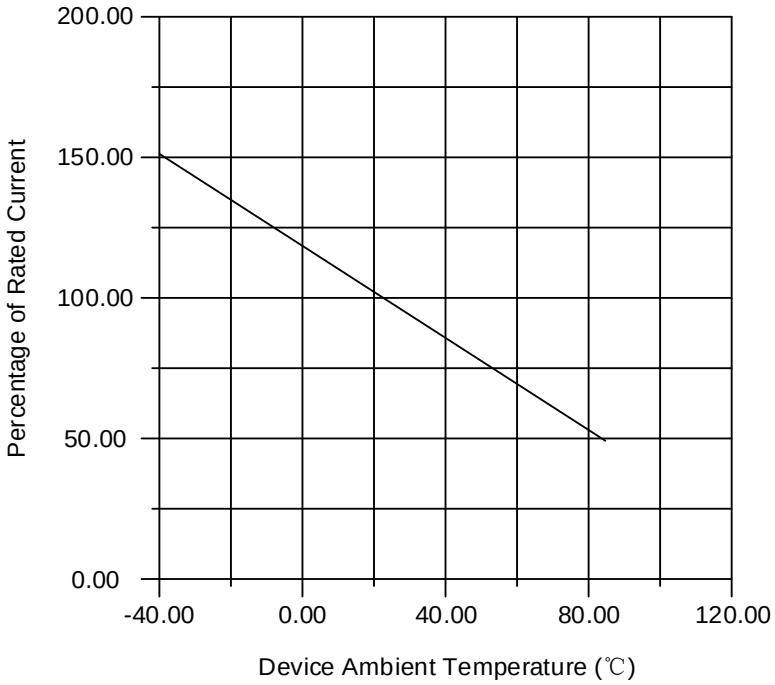
■ Electronic and Thermal Effect

- PPTC devices are secondary protection devices and are used solely for sporadic, accidental over-current or over temperature error condition, and shall NOT be used if or when constant or repeated fault conditions (such fault conditions may becaused by,among others, incorrect pin-connection of a connector) or over-extensive trip events may occur.
- PPTC devices are different from fuses and, when a fault condition occurs, will go into high-resistance state and do not open circuit, in which case the voltage at such PPTC devices may reach a hazardous level.
- Operation over the maximum rating or other forms of improper use may cause failure, arcing, flame and/or other damage to the PPTC devices.
- Conductive material contamination, such as metal particle, may induce shortage, flame or arcing.
- Due to the inductance, the operation circuits may generate a circuit voltage (Ldi/dt) above the rated voltage of PPTC devices, which shall not be used under such circumstances.

■ General

- Customers shall evaluate and test the properties of PPTC devices independently to verify and ensure that their individual applications will be met.
- The performance of PPTC devices will be adversely affected if they are improperly used under electronic, thermal and/or mechanical procedures and/or conditions non-conformant to those recommended by manufacturer.
- Customers shall be responsible for determining whether it is necessary to have back-up, failsafe and/or fool-proof protection To avoid or minimize damage that may result from extra-ordinary, irregular function or failure of PPTC devices.
- Any and all responsibilities and liabilities are disclaimed if any item under this notice of warning is not complied with.

Thermal Derating Curve

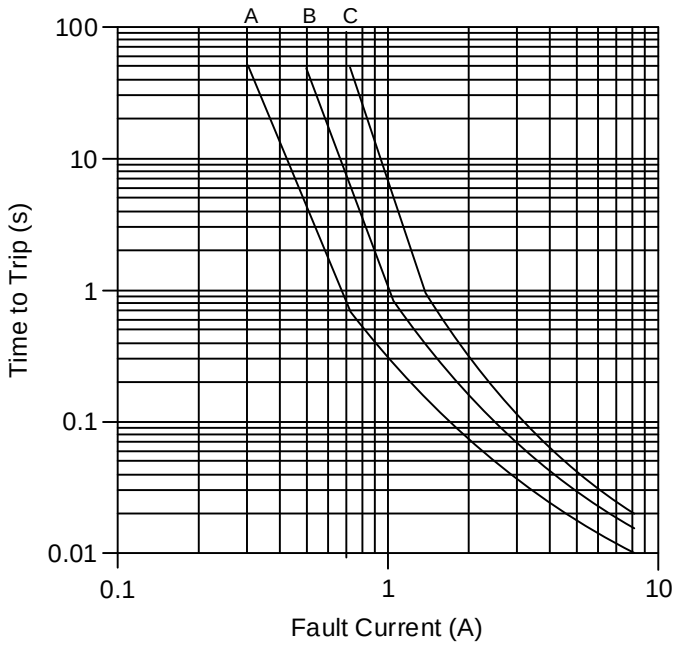


Thermal Derating Chart

Recommended Hold Current (A) at Ambient Temperature (℃)

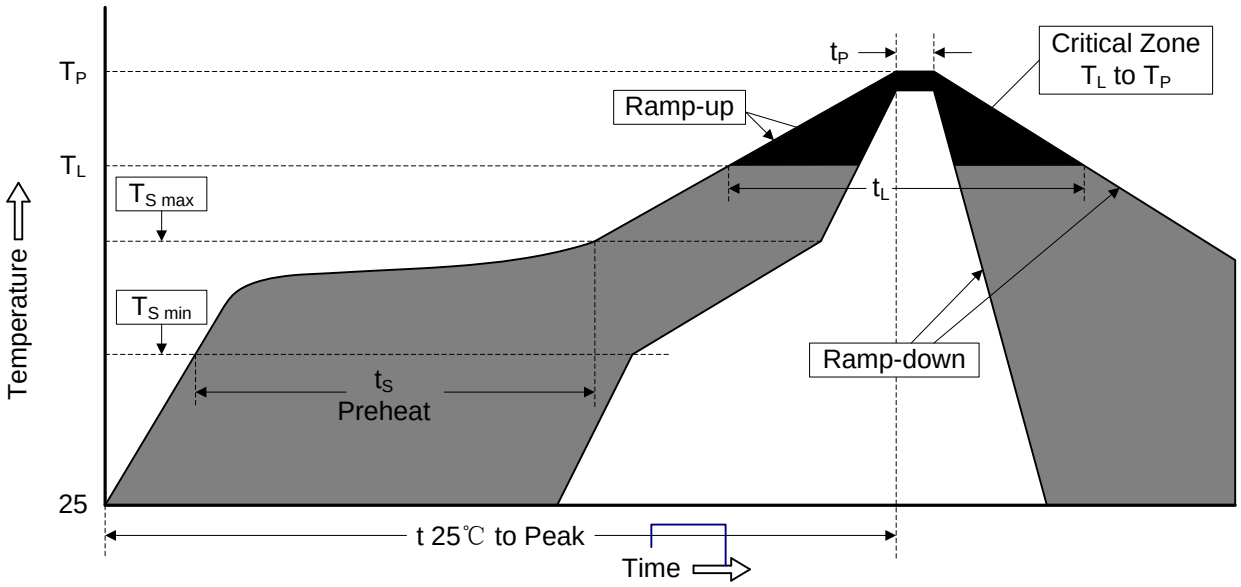
Part Number	Ambient Operation Temperature							
	-40℃	-20℃	0℃	23℃	40℃	60℃	70℃	85℃
SMDL0402B010TF	0.14	0.13	0.11	0.10	0.09	0.07	0.06	0.02
SMDL0402B020TF	0.29	0.26	0.23	0.20	0.18	0.15	0.10	0.03
SMDL0402B035TF	0.50	0.45	0.40	0.35	0.31	0.26	0.18	0.09

Average Time-Current Curve



A-SMD0402B010TF
B-SMD0402B020TF
C-SMD0402B035TF

Soldering Parameters



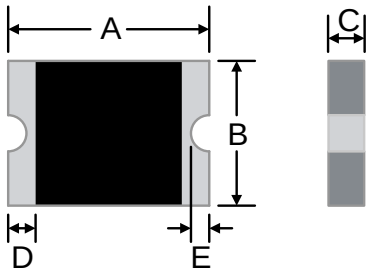
Profile Feature	Pb-Free Assembly
Average ramp-up rate ($T_{S\ max}$ to T_P)	3℃ /second max.
Preheat -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) ($T_{S\ min}$ to $T_{S\ max}$)	 150℃ 200℃ 60-180 seconds
Time maintained above: -Temperature (T_L) -Time (t_L)	 217℃ 60-150 seconds
Peak Temperature (T_P)	260℃
Time within 5℃ of actual Peak Temperature (t_P)	20-40 seconds
Ramp-down Rate	6℃ /second max.
Time 25℃ to Peak Temperature	8 minutes max.

- Recommended reflow methods: IR, vapor phase oven, hot air oven, N_2 environment for lead-free
- Recommended maximum paste thickness is 0.25mm (0.010 inch)
- Device can be cleaned using standard industry methods and solvents.

Note 1: All temperature refer to topside of the package, measured on the package body surface.

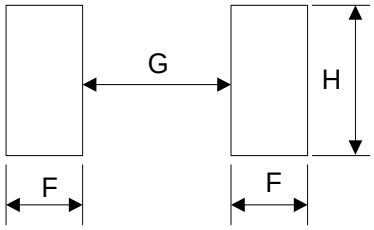
Note 2: If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

Physical Dimensions (mm)



Part Number	A		B		C		D		E		Recommended Pad Layout (typ.)		
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	F	G	H
SMDL0402B010TF	0.85	1.15	0.35	0.65	0.20	0.60	0.10	0.45	-	0.40	0.60	0.40	0.70
SMDL0402B020TF	0.85	1.15	0.35	0.65	0.20	0.60	0.10	0.45	-	0.40	0.60	0.40	0.70
SMDL0402B035TF	0.85	1.15	0.35	0.65	0.20	0.60	0.10	0.45	-	0.40	0.60	0.40	0.70

Recommended Pad Layout



Environmental Specifications

Operating temperature	-40℃ to +85℃
Maximum Device Surface Temperature in Tripped State	125℃
Passive Aging	+85℃, 1000 hours ±50% typical resistance change
Humidity Aging	+85℃, 85%R.H. 100 hours ±100% typical resistance change
Thermal Shock	MIL-STD-202, Method 107G +85℃/-40℃ 20 times -50% typical resistance change
Solvent Resistance	MIL-STD-202, Method 215 No change
Vibration	MIL-STD-883C, Method 2007.1, Condition A No change
Moisture Level Sensitivity	Level 1, J-STD-020C

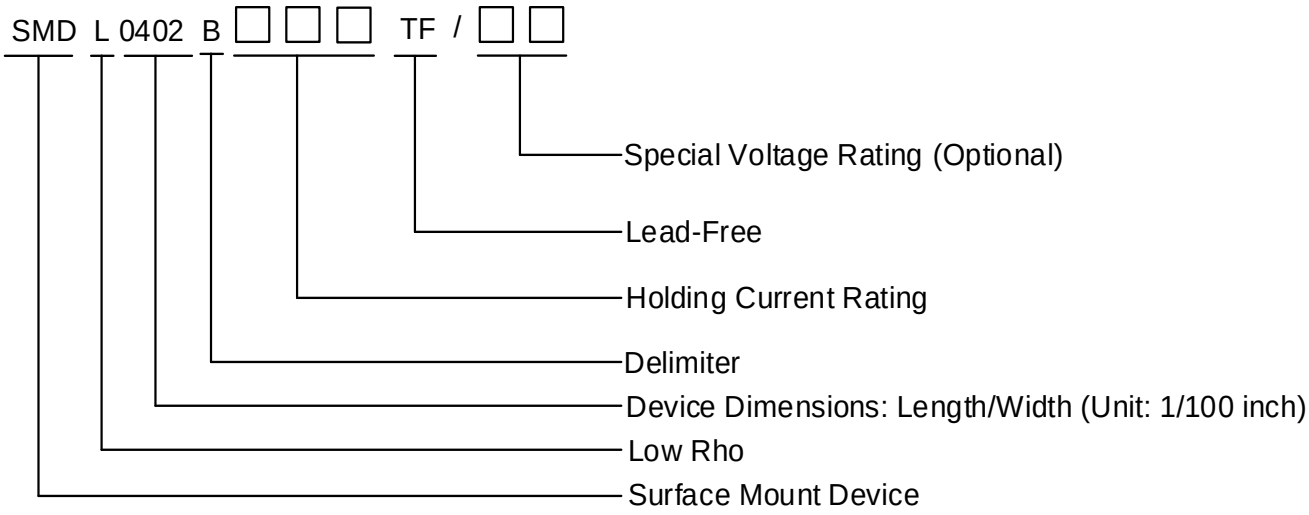
Packaging Quantity and Marking

Part Number	Marking	Quantity	Part Number	Marking	Quantity
SMDL0402B010TF	red	10000	SMDL0402B035TF	white	10000
SMDL0402B020TF	green	10000	/	/	/

Physical Specifications

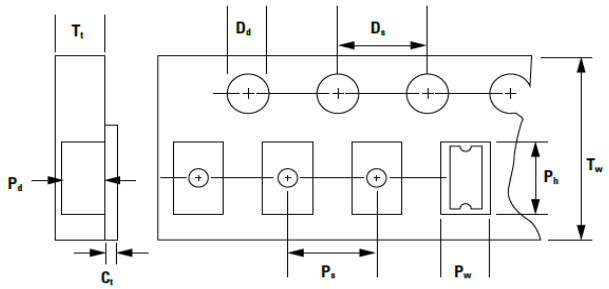
Terminal Material	Solder-Plated Copper (Solder Material: Matte Tin (Sn))
Plating Thickness	Sn：≥15μm ,Cu：≥20μm
Lead Solderability	Meets EIA Specification RS186-9E, ANSI/J-STD-002 Category 3.

Part Number System



Packaging

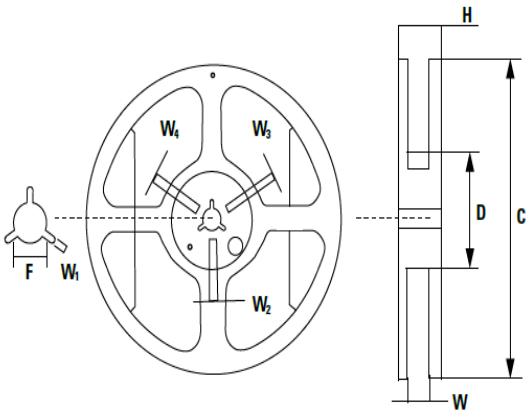
Tape



Symbol	Dimensions (mm)
Ct	0.05±0.01
Dd	1.5±0.1
Ds	4.0±0.1
Pd	0.41±0.1
Ph	1.12±0.1
Ps	2.0±0.1
Pw	0.65±0.03
Tt	0.61±0.1
Tw	8.0±0.1
Leader min.	390
Trailer min.	160

Reel Dimensions: EIA-481-1(mm)

Reel



H	12.0±0.5
W	9.0±0.5
D	Φ60±0.5
F	Φ13.0±0.2
C	Φ178±1
W1	2.2±0.5
W2	3.0±0.5
W3	4.0±0.5
W4	5.5±0.5

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