Innovative Service Around the Globe YAGEO

DATA SHEET

THICK FILM WIDE TERMINAL CHIP RESISTORS AUTOMOTIVE GRADE AND ANTI SURGE

WS series

±5%, ±1%, ±0.5%

Size 0612

RoHS compliant & Halogen free

YAGEO

Product specification – April 10, 2025 V.1



SCOPE

This specification describes WS0612 chip resistors with lead-free terminations made by thick film process.

APPLICATIONS

- All general purpose applications
- Car electronics, industrial application

FEATURES

- AEC-Q200 qualified
- Moisture sensitivity level: MSL 1
- WS series soldering is compliant with J-STD-020D
- Halogen free epoxy
- RoHS compliant
  - Products with lead-free terminations meet RoHS requirements
  - Pb-glass contained in electrodes, resistor element and glass are exempted by RoHS
- Reduce environmentally hazardous waste
- High component and equipment reliability
- The resistors are 100% performed by automatic optical inspection prior to taping.

ORDERING INFORMATION - GLOBAL PART NUMBER

Part number is identified by the series name, size, tolerance, packaging type, temperature coefficient, taping reel and resistance value.

GLOBAL PART NUMBER

WS XXXX X X X XX XXXX L
(1) (2) (3) (4) (5) (6) (7)

(1) SIZE

0612

(2) TOLERANCE

D = ±0.5%

F = ±1%

J = ±5%

"-" = Jumper ordering

(3) PACKAGING TYPE

R = Paper taping reel K = Embossed taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

– = Base on spec

(5) TAPING REEL

07 = 7 inch dia. Reel & Standard power

7W = 7 inch dia. Reel & High power

13 = 13 inch dia. Reel

(6) RESISTANCE VALUE

1Ω to 100 KΩ

There are 2~4 digits indicated the resistance value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g.1K2, not 1K20.

Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter L is the system default code for ordering only. (Note)

Resistance rule of global part number

Resistance coding rule	Example
XRXX (1 to 9.76 Ω)	1R = 1 Ω 1R5 = 1.5 Ω 9R76 = 9.76 Ω
XXRX (10 to 97.6 Ω)	10R = 10 Ω 97R6 = 97.6 Ω
XXXR (100 to 976 Ω)	100R = 100 Ω 976R = 976 Ω
XKXX (1 to 9.76 KΩ)	1K = 1,000 Ω 9K76 = 9760 Ω
XMXX (1 to 9.76 MΩ)	1M = 1,000,000 Ω 9M76= 9,760,000 Ω
XXMX (10 MΩ)	10M = 10,000,000 Ω

ORDERING EXAMPLE

The ordering code for an WS0612 chip resistor, value 1 KΩ with ±1% tolerance, supplied in 7-inch tape reel is: WS0612FR-071KL.

NOTE

1. All our R-Chip products are RoHS compliant and Halogen free. "LFP" of the internal 2D reel label states "Lead-Free Process".
2. On customized label, "LFP" or specific symbol can be printed.

MARKING

WS0612

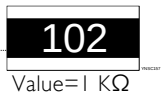


Fig. 1 Value=1 KΩ

E-24 series: 3 digits, ±5%
First two digits for significant figure and 3rd digit for number of zeros

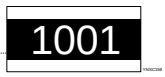


Fig. 2 Value=1 KΩ

E-24/ E-96 series: 4 digits, ±1% & ±0.5%
First three digits for significant figure and 4th digit for number of zeros

CONSTRUCTION

The resistors are constructed on top of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by the resistive glaze. The resistive glaze is covered by a leadfree glass. The composition of the glaze is adjusted to give the approximate required resistance value and laser trimming of this resistive glaze achieves the value inside tolerance. The whole element is covered by a protective overcoat. Size 0508 and bigger is marked with the resistance value on top. Finally, the two external terminations (Ni / matte tin) are added. See fig.3.

OUTLINES

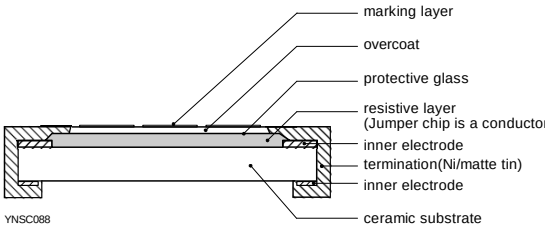


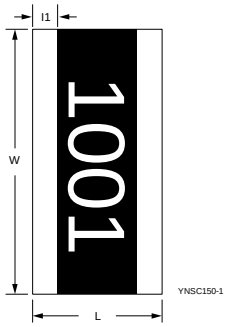
Fig. 3 Chip resistor outlines

DIMENSIONS

Table 1 For outlines, please refer to Fig. 4

TYPE	L (mm)	W (mm)	H (mm)	I_1 (mm)	I_2 (mm)
WS0612	1.60±0.20	3.20 ±0.20	0.55±0.10	0.18±0.15	0.40±0.15

For dimension, please refer to Table 1



Side view for all type

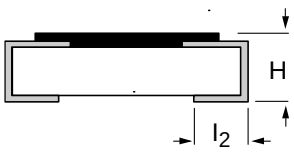


Fig. 4 Chip resistor dimensions

ELECTRICAL CHARACTERISTICS

Table 2

TYPE	RESISTANCE RANGE	CHARACTERISTICS					
		Operating Temperature Range	Max. Working Voltage	Max. Overload Voltage	Dielectric Withstanding Voltage	Temperature Coefficient of Resistance	Jumper Criteria
WS0612	5% (E24) 1Ω to 100 KΩ 0.5%, 1% (E24/E96) 1Ω to 100 KΩ	–55 °C to +155 °C	200V	400V	500V	1 Ω ≤ R < 10 Ω, ±200ppm/°C 10 Ω≤ R ≤100 KΩ, ±100ppm/°C	Rated Current 2A Max. Current 10A

FOOTPRINT AND SOLDERING PROFILES

Please refer to data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	WS0612
Paper taping reel (R)	7" (178 mm)	5,000
	13" (330 mm)	20,000
Embossed taping reel (K)	7" (178 mm)	---

NOTE

1. For paper/embossed tape and reel specifications/dimensions, please refer to data sheet "Chip resistors packing".

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

Range: –55 °C to +155 °C

POWER RATING

Each type rated power at 70 °C:
WS0612 =1W, 1.5W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$V = \sqrt{(P \times R)}$

Or Maximum working voltage whichever is less

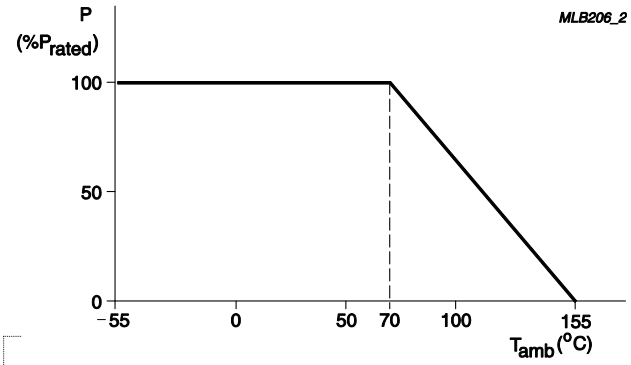


Fig. 5 Maximum dissipation (P_{max}) in percentage of rated power as a function of the operating ambient temperature (T_{amb})

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

R = Resistance value (Ω)

PULSE LOAD BEHAVIOR

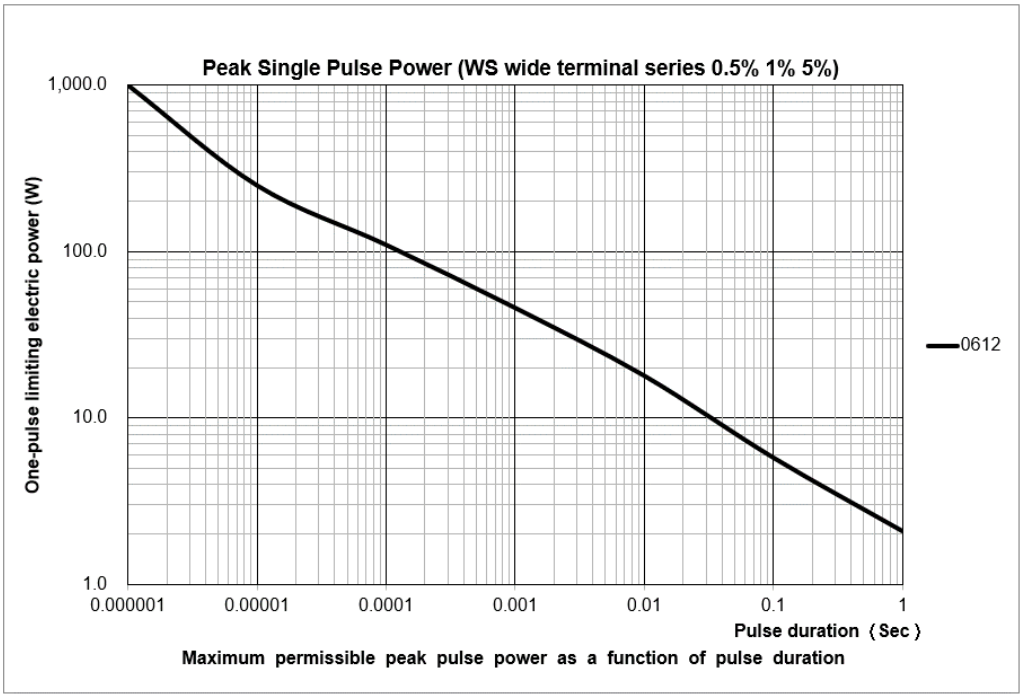


Fig. 6-1 Pulse-Load behavior

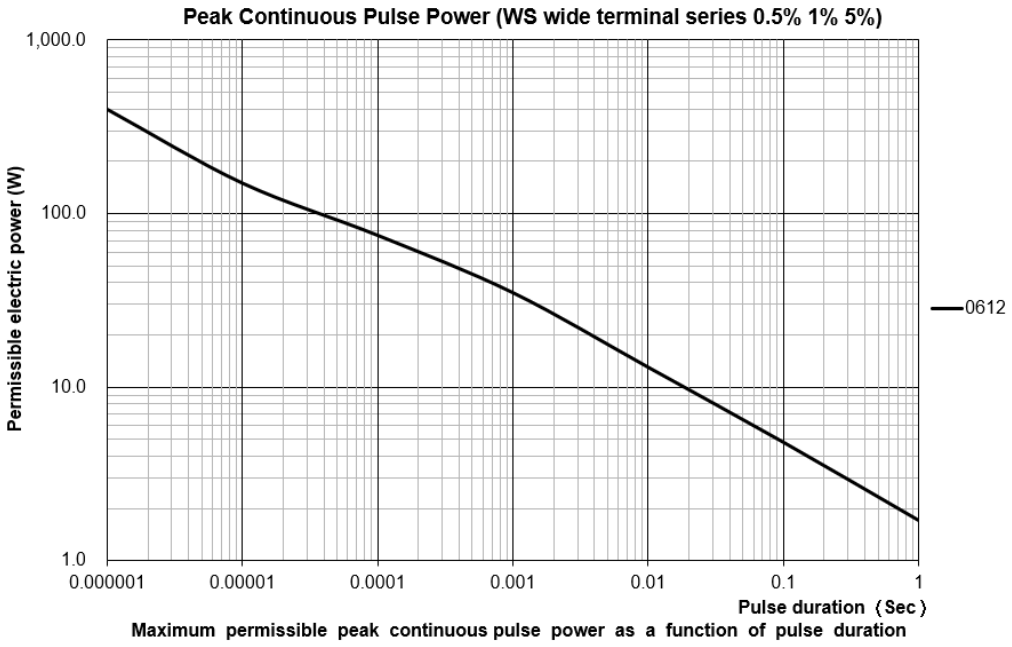


Fig. 6-2 Pulse-Load behavior

TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
High Temperature Exposure	AEC-Q200 Test 3 MIL-STD-202 Method 108	1,000 hours at T_A = 155 °C, unpowered	±(1.0%+0.05Ω) for D/F tol ±(2.0%+0.05Ω) for J tol <50 mΩ for Jumper
Moisture Resistance	MIL-STD-202 Method 106	Each temperature / humidity cycle is defined at 8 hours, 3 cycles / 24 hours for 10d. with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered Parts mounted on test-boards, without condensation on parts	±(0.5%+0.05Ω) for D/F tol ±(2.0%+0.05Ω) for J tol <100 mΩ for Jumper
Biased Humidity	AEC-Q200 Test 7 MIL-STD-202 Method 103	1,000 hours; 85 °C / 85% RH 10% of operating power Measurement at 24±4 hours after test conclusion.	±(1.0%+0.05Ω) for D/F tol ±(3.0%+0.05Ω) for J tol <100 mΩ for Jumper
Operational Life	AEC-Q200 Test 8 MIL-STD-202 Method 108	1,000 hours at 125 °C, derated voltage applied for 1.5 hours on, 0.5 hour off, still-air required	±(1.0%+0.05Ω) for D/F tol ±(3.0%+0.05Ω) for J tol <100 mΩ for Jumper
Resistance to Soldering Heat	AEC-Q200 Test 15 MIL-STD-202 Method 210	Condition B, no pre-heat of samples Lead-free solder, 260±5 °C, 10±1 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	±(0.5%+0.05Ω) for D/F tol ±(1.0%+0.05Ω) for J tol <50 mΩ for Jumper No visible damage
Thermal Shock	MIL-STD-202 Method 107	-55/+125 °C Number of cycles is 300. Devices mounted Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	±(0.5%+0.05Ω) for D/F tol ±(1.0%+0.05Ω) for J tol <50 mΩ for Jumper
ESD	AEC-Q200 Test 17 AEC-Q200-002	Human Body Model, 1 $_{pos.}$ + 1 $_{neg.}$ discharges 0612 and above: 2KV	±(3.0%+0.05Ω) <50 mΩ for Jumper

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability - Wetting	AEC-Q200 Test 18 J-STD-002	Electrical Test not required Magnification 50X SMD conditions: (a) Method B, aging 4 hours at 155 °C dry heat, dipping at 235±3 °C for 5±0.5 seconds. (b) Method B, steam aging 8 hours, dipping at 215±3 °C for 5±0.5 seconds. (c) Method D, steam aging 8 hours, dipping at 260±3 °C for 30±0.5 seconds.	Well tinned (≥95% covered) No visible damage
Board Flex	AEC-Q200 Test 21 AEC-Q200-005	Chips mounted on a 100mm x 40mm glass epoxy resin PCB (FR4) Bending for 0612 and above: 2 mm Holding time: minimum 60 seconds	±(1.0%+0.05Ω) <50 mΩ for Jumper
Temperature Coefficient of Resistance (T.C.R.)	MIL-STD-202 Method 304	At +25/–55 °C and +25/+125 °C **Formula:** $T.C.R=\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6$ (ppm/°C) Where t_1=+25 °C or specified room temperature t_2=–55 °C or +125 °C test temperature R_1=resistance at reference temperature in ohms R_2=resistance at test temperature in ohms	Refer to table 2
Short Time Overload	IEC60115-1 8.1	2.5 times of rated voltage or maximum overload voltage whichever is less for 1225 : 2s 0612/2010: 5s at room temperature	±(1.0%+0.05Ω) for D/F tol ±(2.0%+0.05Ω) for J tol <50 mΩ for Jumper

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 1	Apr. 10, 2025	-	- Extend the resistance range and add 1.5W for high power.
Version 0	Jan. 10, 2024	-	- First issue of this specification

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