

C0805C102J5GECTU

Aliases (C0805C102J5GEC7800)

ESD SMD Comm COG, Ceramic, 1,000 pF, 5%, 50 VDC, COG, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I, 0.7 mm, 0805 / 2012



Click [here](#) for the 3D model.

General Information

Series	ESD SMD Comm COG
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I
Features	Temperature Stable, Low ESR, Class I
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	13 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	1,000 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
ESD Level per AEC-Q200	8,000 V ESD Level
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

Dimensions

L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.9mm +/-0.10mm
S	0.7mm MIN
B	0.5mm +/-0.25mm
Case Code (EIA / mm)	0805 / 2012

Packaging Specifications

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

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