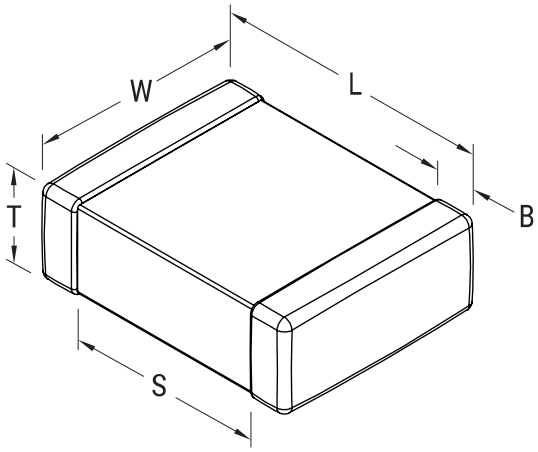
C1812C103J2GACTU

Aliases (C1812C103J2GAC7800)

SMD Comm C0G, Ceramic, 0.01 uF, 5%, 200 VDC, C0G, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 1812 / 4532



Click here for the 3D model.

General Information	
Series	SMD Comm C0G
Style	SMD Chip
Description	SMD, MLCC, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	67 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
T	1mm +/-0.10mm
S	2.3mm MIN
B	0.6mm +/-0.35mm
Case Code (EIA / mm)	1812 / 4532

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

Specifications	
Capacitance	0.01 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	200 VDC
Dielectric Withstanding Voltage	500 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	C0G
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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