

TOP VIEW  
Single or Double Chip Stack

PROFILE VIEW  
Single Chip Stack



Click [here](#) for the 3D model.

**General Information**

Series	KPS SMD Comm X7R SnPb
Style	Stacked Chip
Description	SMD, MLCC, Stacked, Single Chip, Temperature Stable
Features	Temperature Stable
RoHS	No
Prop 65	<b>WARNING:</b> Cancer and reproductive harm - <a href="https://www.p65warnings.ca.gov/">https://www.p65warnings.ca.gov/</a>
SCIP Number	795b247e-78a9-405c-8728-223c2d9782a2
Termination	Lead (SnPb)
Typical Component Weight	410 mg
Shelf Life	78 Weeks
MSL	1

**Dimensions**

L	6mm +/-0.5mm
W	5mm +/-0.5mm
T	3.5mm +/-0.30mm
B	1.6mm +/-0.3mm

**Packaging Specifications**

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	300

**Specifications**

Capacitance	22 uF
Measurement Condition	120 Hz 0.5Vrms
Tolerance	20%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 120Hz 0.5Vrms
Dissipation Factor	3.5% 120 Hz 0.5Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	22.7 MOhms

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